

AMENDMENT TO THE CLAIMS:

1 - 16 (Cancelled)

17. (Original) A method for forming a semiconductor apparatus comprising the steps of:
forming an N+ diffusion and a P+ diffusion;
forming a polysilicon line, the polysilicon line having a P+ region and an N+ region, the
polysilicon line having an N+/P+ junction area wherein said junction area comprises the area
where the P+ region of the polysilicon line and the N+ region of the polysilicon line abut each
other; and,
selectively forming a silicide strap extending across the junction area, wherein the silicide strap
forms an electrical connection between the P+ region of the polysilicon line and the N+ region of
the polysilicon line; and
selectively preventing the formation of silicide on the N+ diffusion and the P+ diffusion.

18. (Original) The method of claim 17 wherein the step of selectively forming a silicide strap
comprises:
forming a hard mask on the semiconductor structure;
 patterning the hard mask to expose the N+/P+ junction area; and
 forming silicide in the exposed N+/P+ junction area.

19. (Original) The method of claim 17 wherein the step of selectively preventing the formation of silicide on the N+ diffusion and the P+ diffusion comprises:

forming a hard mask on the semiconductor structure; and

patterning the hard mask to expose portions of the semiconductor structure, said patterning not exposing the N+ diffusion and the P+ diffusion.

20. (Original) The method of claim 17 further comprising the step of:

completing devices and back end of line processes.

21. (Original) The method of claim 17 wherein the semiconductor structure is part of an SRAM.

22. (Original) The method of claim 17 wherein current leakage is reduced by selectively preventing silicide formation on the N+ diffusion and P+ diffusion.

23. (Original) The method of claim 22 wherein the current leakage reduced comprises Gate Induced Drain Leakage (GIDL).

REMARKS

Favorable consideration and allowance of the claims of the present application, as amended herein, is respectfully requested.

In this preliminary amendment, Applicants have cancelled original Claims 1-16.

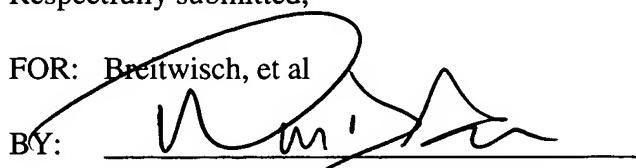
Applicants have also amended the specification to include reference to the parent application.

Since the above amendments to the claims and specification do not introduce new matter into the application, entry thereof is respectfully requested.

Consideration and allowance of the claims of the present application are thus respectfully requested.

Respectfully submitted,

FOR: Breitwisch, et al
BY:


William D. Sabe, Reg. No. 27,465

Dated: Nov. 26, 2003
IBM Corporation
1000 River Street, 972E
Essex Junction, VT 05452
(802)769-9454